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--- Checking for multiple reference of part in schematics ---
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C5106  VRD100-120Sch_HiFiDeck1.DWG  54.6600 36.2100
C5106  VRD100-120Sch_HiFiDeck2.DWG  54.6600 36.2100

C6101  VRD100Sch_LogicDeck1.DWG  6.1400 12.5570
C6101  VRD120Sch_LogicDeck1.DWG  6.1400 12.7166

C6113  VRD120Sch_LogicDeck1.DWG  10.7551 11.1125
C6113  VRD100Sch_LogicDeck1.DWG  10.7551 11.1125

C6114  VRD100Sch_LogicDeck1.DWG  -5.5992 2.6325
C6114  VRD120Sch_LogicDeck1.DWG  -5.5992 2.6325

C6117  VRD100Sch_LogicDeck1.DWG  0.9100 3.7800
C6117  VRD120Sch_LogicDeck1.DWG  0.9100 3.7800

C6118  VRD100Sch_LogicDeck1.DWG  0.2700 4.2725
C6118  VRD120Sch_LogicDeck1.DWG  0.2700 4.3800

C6122  VRD120Sch_LogicDeck1.DWG  7.3900 -2.1275
C6122  VRD100Sch_LogicDeck1.DWG  7.3900 -2.1275

C6124  VRD100Sch_LogicDeck1.DWG  7.2200 11.1500
C6124  VRD120Sch_LogicDeck1.DWG  7.5400 10.8700

C6126  VRD120Sch_LogicDeck1.DWG  -3.4100 9.9000
C6126  VRD100Sch_LogicDeck1.DWG  -3.4100 9.9000

C6127  VRD100Sch_LogicDeck1.DWG  -4.3700 9.8725
C6127  VRD120Sch_LogicDeck1.DWG  -4.3700 9.8725

C6129  VRD100Sch_LogicDeck1.DWG  -2.4289 5.2325
C6129  VRD120Sch_LogicDeck1.DWG  -2.4289 5.2325

C6130  VRD100Sch_LogicDeck1.DWG  6.3800 10.8049
C6130  VRD120Sch_LogicDeck1.DWG  6.3800 10.8049

C6132  VRD120Sch_LogicDeck1.DWG  6.9000 10.7100
C6132  VRD100Sch_LogicDeck1.DWG  6.9000 10.7100

C6133  VRD100Sch_LogicDeck1.DWG  -4.4822 2.4532
C6133  VRD120Sch_LogicDeck1.DWG  -4.4822 2.4532

C6134  VRD100Sch_LogicDeck1.DWG  8.7900 12.7525
C6134  VRD120Sch_LogicDeck1.DWG  8.7900 12.7525

C6136  VRD120Sch_LogicDeck1.DWG  19.0648 7.4325
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C6136  VRD100Sch_LogicDeck1.DWG  19.0648 7.4325

C6137  VRD100Sch_LogicDeck1.DWG  15.8459 7.4325
C6137  VRD120Sch_LogicDeck1.DWG  15.8459 7.4325

C6138  VRD120Sch_LogicDeck1.DWG  -2.6422 11.5800
C6138  VRD100Sch_LogicDeck1.DWG  -2.6422 11.5800

C6140  VRD100Sch_LogicDeck1.DWG  -8.7941 0.9925
C6140  VRD120Sch_LogicDeck1.DWG  -8.7941 0.9925

C6141  VRD120Sch_LogicDeck1.DWG  12.5460 11.1125
C6141  VRD100Sch_LogicDeck1.DWG  12.5460 11.1125

C6147  VRD120Sch_LogicDeck1.DWG  -9.5941 -1.5275
C6147  VRD100Sch_LogicDeck1.DWG  -9.5941 -1.5275

C6148  VRD100Sch_LogicDeck1.DWG  -8.4741 -1.5275
C6148  VRD120Sch_LogicDeck1.DWG  -8.4741 -1.5275

C6150  VRD120Sch_LogicDeck1.DWG  -4.7700 -4.0200
C6150  VRD100Sch_LogicDeck1.DWG  -4.7700 -4.0200

C6151  VRD120Sch_LogicDeck1.DWG  -5.4991 -4.1275
C6151  VRD100Sch_LogicDeck1.DWG  -5.4991 -4.1275

C6155  VRD120Sch_LogicDeck1.DWG  -7.5400 0.9100
C6155  VRD100Sch_LogicDeck1.DWG  -7.5400 0.9100

C6156  VRD100Sch_LogicDeck1.DWG  -8.1246 0.8985
C6156  VRD120Sch_LogicDeck1.DWG  -8.1246 0.8985

C6157  VRD120Sch_LogicDeck1.DWG  -7.9815 -4.1554
C6157  VRD100Sch_LogicDeck1.DWG  -7.9815 -4.1554

C6158  VRD120Sch_LogicDeck1.DWG  -6.6500 -4.1275
C6158  VRD100Sch_LogicDeck1.DWG  -6.6500 -4.1275

C6159  VRD100Sch_LogicDeck1.DWG  1.7311 -0.0875
C6159  VRD120Sch_LogicDeck1.DWG  1.7311 -0.0875

C6160  VRD100Sch_LogicDeck1.DWG  10.1554 6.1385
C6160  VRD120Sch_LogicDeck1.DWG  10.1554 6.1385

C6161  VRD120Sch_LogicDeck1.DWG  10.8600 6.1900
C6161  VRD100Sch_LogicDeck1.DWG  10.8600 6.2300

C6166  VRD120Sch_LogicDeck1.DWG  0.6859 9.1400
C6166  VRD100Sch_LogicDeck1.DWG  0.6859 9.1400
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C6167  VRD120Sch_LogicDeck1.DWG  8.3400 10.8300
C6167  VRD100Sch_LogicDeck1.DWG  8.3400 10.8300

C6201  VRD120Sch_LogicDeck2.DWG  6.1400 12.5570
C6201  VRD100Sch_LogicDeck2.DWG  6.1400 12.5570

C6203  VRD100Sch_LogicDeck2.DWG  16.2059 3.3525
C6203  VRD120Sch_LogicDeck2.DWG  16.2059 3.3525

C6213  VRD100Sch_LogicDeck2.DWG  10.7551 11.1125
C6213  VRD120Sch_LogicDeck2.DWG  10.7551 11.1125

C6214  VRD120Sch_LogicDeck2.DWG  -7.4100 2.7675
C6214  VRD100Sch_LogicDeck2.DWG  -7.3192 2.8325

C6215  VRD100Sch_LogicDeck2.DWG  0.9100 4.7800
C6215  VRD120Sch_LogicDeck2.DWG  0.9100 4.7800

C6216  VRD120Sch_LogicDeck2.DWG  0.2700 5.2325
C6216  VRD100Sch_LogicDeck2.DWG  0.2700 5.2325

C6217  VRD120Sch_LogicDeck2.DWG  0.9500 3.7800
C6217  VRD100Sch_LogicDeck2.DWG  0.9500 3.7800

C6218  VRD120Sch_LogicDeck2.DWG  0.2700 4.2725
C6218  VRD100Sch_LogicDeck2.DWG  0.2700 4.2725

C6220  VRD100Sch_LogicDeck2.DWG  -6.4741 6.7125
C6220  VRD120Sch_LogicDeck2.DWG  -6.4741 6.7125

C6221  VRD100Sch_LogicDeck2.DWG  -5.0706 6.5646
C6221  VRD120Sch_LogicDeck2.DWG  -5.0706 6.5646

C6222  VRD100Sch_LogicDeck2.DWG  -6.1415 4.9496
C6222  VRD120Sch_LogicDeck2.DWG  -6.1415 4.9496

C6224  VRD120Sch_LogicDeck2.DWG  7.2200 11.1500
C6224  VRD100Sch_LogicDeck2.DWG  7.2200 11.1500

C6225  VRD120Sch_LogicDeck2.DWG  -7.8822 11.4600
C6225  VRD100Sch_LogicDeck2.DWG  -7.8822 11.4600

C6226  VRD100Sch_LogicDeck2.DWG  -5.0900 9.9000
C6226  VRD120Sch_LogicDeck2.DWG  -5.0900 9.9000

C6227  VRD100Sch_LogicDeck2.DWG  -6.0500 9.8725
C6227  VRD120Sch_LogicDeck2.DWG  -6.0500 9.8200

C6228  VRD100Sch_LogicDeck2.DWG  -5.0215 5.2046
C6228  VRD120Sch_LogicDeck2.DWG  -5.0215 5.2046
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C6229  VRD120Sch_LogicDeck2.DWG  -3.9889 5.2325
C6229  VRD100Sch_LogicDeck2.DWG  -3.9889 5.2325

C6230  VRD120Sch_LogicDeck2.DWG  6.3800 10.8049
C6230  VRD100Sch_LogicDeck2.DWG  6.3800 10.8049

C6232  VRD120Sch_LogicDeck2.DWG  6.9000 10.7100
C6232  VRD100Sch_LogicDeck2.DWG  6.9000 10.7100

C6233  VRD100Sch_LogicDeck2.DWG  -6.0822 2.4532
C6233  VRD120Sch_LogicDeck2.DWG  -6.0822 2.4532

C6234  VRD100Sch_LogicDeck2.DWG  8.7900 12.7525
C6234  VRD120Sch_LogicDeck2.DWG  8.7900 12.7525

C6236  VRD100Sch_LogicDeck2.DWG  19.0648 7.4325
C6236  VRD120Sch_LogicDeck2.DWG  19.0648 7.4325

C6237  VRD120Sch_LogicDeck2.DWG  15.8459 7.4325
C6237  VRD100Sch_LogicDeck2.DWG  15.8459 7.4325

C6238  VRD120Sch_LogicDeck2.DWG  -4.3222 11.5800
C6238  VRD100Sch_LogicDeck2.DWG  -4.3222 11.5800

C6240  VRD100Sch_LogicDeck2.DWG  -10.6741 0.9925
C6240  VRD120Sch_LogicDeck2.DWG  -10.6741 0.9925

C6241  VRD120Sch_LogicDeck2.DWG  12.5460 11.1125
C6241  VRD100Sch_LogicDeck2.DWG  12.5460 11.1125

C6243  VRD100Sch_LogicDeck2.DWG  10.7900 0.3925
C6243  VRD120Sch_LogicDeck2.DWG  10.7900 0.3925

C6247  VRD120Sch_LogicDeck2.DWG  -10.7300 0.4200
C6247  VRD100Sch_LogicDeck2.DWG  -10.7300 0.4200

C6248  VRD120Sch_LogicDeck2.DWG  -9.4100 -0.1000
C6248  VRD100Sch_LogicDeck2.DWG  -9.4100 -0.1000

C6250  VRD120Sch_LogicDeck2.DWG  -10.7300 -2.4600
C6250  VRD100Sch_LogicDeck2.DWG  -10.7300 -2.4600

C6251  VRD120Sch_LogicDeck2.DWG  -11.4432 -1.7275
C6251  VRD100Sch_LogicDeck2.DWG  -11.4432 -1.7275

C6255  VRD120Sch_LogicDeck2.DWG  -9.4200 0.9100
C6255  VRD100Sch_LogicDeck2.DWG  -9.4200 0.9100

C6256  VRD100Sch_LogicDeck2.DWG  -10.0046 0.8985
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C6256  VRD120Sch_LogicDeck2.DWG  -10.0046 0.8985

C6257  VRD120Sch_LogicDeck2.DWG  -9.9700 -2.3000
C6257  VRD100Sch_LogicDeck2.DWG  -9.9700 -2.3000

C6258  VRD120Sch_LogicDeck2.DWG  -9.2900 -1.6600
C6258  VRD100Sch_LogicDeck2.DWG  -9.2900 -1.6600

C6259  VRD120Sch_LogicDeck2.DWG  1.7311 -0.0875
C6259  VRD100Sch_LogicDeck2.DWG  1.7311 -0.0875

C6260  VRD120Sch_LogicDeck2.DWG  10.1554 6.2870
C6260  VRD100Sch_LogicDeck2.DWG  10.1554 6.1385

C6261  VRD100Sch_LogicDeck2.DWG  10.8600 6.2300
C6261  VRD120Sch_LogicDeck2.DWG  10.7800 6.3500

C6CM01  VRD100Sch_LogicDeck1.DWG  15.7348 2.3646
C6CM01  VRD120Sch_LogicDeck1.DWG  15.7348 2.3646

C6CM02  VRD100Sch_LogicDeck1.DWG  16.7240 2.6614
C6CM02  VRD120Sch_LogicDeck1.DWG  16.7240 2.6614

C6CM03  VRD120Sch_LogicDeck1.DWG  17.2040 2.3800
C6CM03  VRD100Sch_LogicDeck1.DWG  17.2040 2.3800

C6CM04  VRD120Sch_LogicDeck1.DWG  17.4374 2.0600
C6CM04  VRD100Sch_LogicDeck1.DWG  17.4374 2.0600

C6CM05  VRD100Sch_LogicDeck1.DWG  17.2800 1.6600
C6CM05  VRD120Sch_LogicDeck1.DWG  17.2800 1.6600

C6CM06  VRD120Sch_LogicDeck1.DWG  17.3374 1.3885
C6CM06  VRD100Sch_LogicDeck1.DWG  17.3374 1.3885

C6CM07  VRD100Sch_LogicDeck1.DWG  17.6040 0.3282
C6CM07  VRD120Sch_LogicDeck1.DWG  17.6040 0.3282

C6CM08  VRD120Sch_LogicDeck1.DWG  16.6556 0.6922
C6CM08  VRD100Sch_LogicDeck1.DWG  16.6556 0.6922

C6CM09  VRD100Sch_LogicDeck1.DWG  16.3768 -2.3000
C6CM09  VRD120Sch_LogicDeck1.DWG  16.3768 -2.3000

C6CM11  VRD100Sch_LogicDeck1.DWG  17.0040 -1.6600
C6CM11  VRD120Sch_LogicDeck1.DWG  17.3164 -1.6200

C6CM12  VRD120Sch_LogicDeck1.DWG  20.7640 -3.3000
C6CM12  VRD100Sch_LogicDeck1.DWG  20.7640 -3.3000
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C6CM13  VRD120Sch_LogicDeck1.DWG  20.7640 -2.9400
C6CM13  VRD100Sch_LogicDeck1.DWG  20.7640 -2.9400

C6CM14  VRD120Sch_LogicDeck1.DWG  20.7614 -2.6200
C6CM14  VRD100Sch_LogicDeck1.DWG  20.7614 -2.6200

C6CM15  VRD120Sch_LogicDeck1.DWG  21.0450 -1.9168
C6CM15  VRD100Sch_LogicDeck1.DWG  21.0450 -1.9168

C6CM16  VRD100Sch_LogicDeck1.DWG  20.8040 -1.6200
C6CM16  VRD120Sch_LogicDeck1.DWG  20.8040 -1.6200

C6CM17  VRD120Sch_LogicDeck1.DWG  21.1614 -0.3400
C6CM17  VRD100Sch_LogicDeck1.DWG  21.1614 -0.3400

C6CM18  VRD100Sch_LogicDeck1.DWG  21.1240 0.7000
C6CM18  VRD120Sch_LogicDeck1.DWG  21.1240 0.7000

C6CM19  VRD120Sch_LogicDeck1.DWG  21.4866 1.0200
C6CM19  VRD100Sch_LogicDeck1.DWG  21.4866 1.0200

C6CM20  VRD100Sch_LogicDeck1.DWG  21.4840 1.3800
C6CM20  VRD120Sch_LogicDeck1.DWG  21.4840 1.3800

C6CM21  VRD100Sch_LogicDeck1.DWG  20.7436 1.7083
C6CM21  VRD120Sch_LogicDeck1.DWG  20.7436 1.7083

C6CM22  VRD100Sch_LogicDeck1.DWG  16.9886 -3.0200
C6CM22  VRD120Sch_LogicDeck1.DWG  17.4275 -2.9800

C6CM23  VRD120Sch_LogicDeck1.DWG  17.4275 -0.3000
C6CM23  VRD100Sch_LogicDeck1.DWG  17.4275 -0.3000

C6CM24  VRD120Sch_LogicDeck1.DWG  20.8440 2.0200
C6CM24  VRD100Sch_LogicDeck1.DWG  20.8440 2.0200

C6CM25  VRD120Sch_LogicDeck1.DWG  18.6275 4.1150
C6CM25  VRD100Sch_LogicDeck1.DWG  18.6275 4.1150

C701  VRD120Sch_LogicDeck1.DWG  -3.8744 -5.0875
C701  VRD100Sch_LogicDeck1.DWG  -3.8744 -5.0875

C702  VRD100Sch_LogicDeck1.DWG  -3.1726 -5.1154
C702  VRD120Sch_LogicDeck1.DWG  -3.1726 -5.1154

C703  VRD100Sch_FunctionTimer.DWG  35.6005 13.7852
C703  VRD120Sch_FunctionTimer.DWG  -18.2800 22.0675

C704  VRD120Sch_FunctionTimer.DWG  -19.9896 22.0210
C704  VRD100Sch_FunctionTimer.DWG  33.8965 13.7387
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C705  VRD120Sch_FunctionTimer.DWG  -21.9200 21.0012
C705  VRD100Sch_FunctionTimer.DWG  30.8657 12.8131

CN101  VRD100-120Sch_Power.DWG  39.3729 8.2813
CN101  VRD100-120Sch_Power.DWG  38.7307 24.6530

CN102  VRD100Sch_LogicDeck2.DWG  -15.6371 -4.9525
CN102  VRD120Sch_LogicDeck2.DWG  -14.7257 -4.8525

CN6101  VRD100Sch_LogicDeck1.DWG  -10.9216 15.7159
CN6101  VRD120Sch_LogicDeck1.DWG  -10.7911 15.6740

CN6102  VRD100Sch_LogicDeck1.DWG  28.4582 -0.0987
CN6102  VRD120Sch_LogicDeck1.DWG  28.7696 -0.0387

CN6104  VRD120Sch_LogicDeck1.DWG  -11.2020 3.9330
CN6104  VRD100Sch_LogicDeck1.DWG  -11.1100 4.0645

CN6105  VRD100Sch_LogicDeck1.DWG  -11.1051 0.5845
CN6105  VRD120Sch_LogicDeck1.DWG  -11.1010 0.6130

CN6125  VRD100Sch_LogicDeck1.DWG  -6.0360 13.1800
CN6125  VRD120Sch_LogicDeck1.DWG  -6.0558 13.1800

CN6201  VRD100Sch_LogicDeck2.DWG  -12.7552 11.8645
CN6201  VRD120Sch_LogicDeck2.DWG  -12.8016 11.5559

CN6202  VRD100Sch_LogicDeck2.DWG  28.6929 -3.4925
CN6202  VRD120Sch_LogicDeck2.DWG  28.2529 -3.6125

CN6204  VRD120Sch_LogicDeck2.DWG  -12.9900 4.0645
CN6204  VRD100Sch_LogicDeck2.DWG  -12.9100 4.5445

CN6205  VRD120Sch_LogicDeck2.DWG  -12.9851 0.5845
CN6205  VRD100Sch_LogicDeck2.DWG  -12.9851 0.5845

CN701  VRD100Sch_LogicDeck1.DWG  -13.3861 9.7013
CN701  VRD120Sch_LogicDeck1.DWG  -13.3114 10.1475

CN703  VRD120Sch_FunctionTimer.DWG  -3.1817 32.2788
CN703  VRD100Sch_FunctionTimer.DWG  60.6183 31.2788

CN704  VRD120Sch_FunctionTimer.DWG  -3.2254 9.8388
CN704  VRD100Sch_FunctionTimer.DWG  60.5746 8.9587

D1SD31  VRD100-120Sch_SMPS.DWG  31.1844 20.3000
D1SD31  VRD100-120Sch_SMPS.DWG  29.6574 21.6842

D6101  VRD100Sch_LogicDeck1.DWG  -3.0694 -2.5587
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D6101  VRD120Sch_LogicDeck1.DWG  -3.0694 -2.5587

D6104  VRD100Sch_LogicDeck1.DWG  -5.8074 4.1613
D6104  VRD120Sch_LogicDeck1.DWG  -5.8074 4.1613

D6201  VRD100Sch_LogicDeck2.DWG  -4.5494 -5.5988
D6201  VRD120Sch_LogicDeck2.DWG  -4.5494 -5.5988

D6203  VRD100Sch_LogicDeck2.DWG  -4.5648 -2.7187
D6203  VRD120Sch_LogicDeck2.DWG  -4.5648 -2.7187

D6204  VRD120Sch_LogicDeck2.DWG  -7.3274 4.1613
D6204  VRD100Sch_LogicDeck2.DWG  -7.3274 4.1613

D701  VRD100Sch_LogicDeck1.DWG  -3.9519 -3.9587
D701  VRD120Sch_LogicDeck1.DWG  -3.9519 -3.9587

D702  VRD100Sch_FunctionTimer.DWG  28.2400 22.7000
D702  VRD120Sch_FunctionTimer.DWG  -36.2400 23.4525

D703  VRD100Sch_FunctionTimer.DWG  28.2400 22.0600
D703  VRD120Sch_FunctionTimer.DWG  -36.2400 24.7325

D704  VRD120Sch_FunctionTimer.DWG  -36.2443 26.0125
D704  VRD100Sch_FunctionTimer.DWG  28.2357 23.3400

D705  VRD100Sch_FunctionTimer.DWG  28.2400 24.6200
D705  VRD120Sch_FunctionTimer.DWG  -36.2400 24.0925

DT701  VRD100Sch_FunctionTimer.DWG  5.7086 47.4300
DT701  VRD120Sch_FunctionTimer.DWG  -58.7714 48.8225

IC6102  VRD120Sch_LogicDeck1.DWG  -3.3220 13.4930
IC6102  VRD100Sch_LogicDeck2.DWG  -5.0020 13.4930
IC6102  VRD100Sch_LogicDeck1.DWG  -3.3220 13.4930

IC6201  VRD100Sch_LogicDeck2.DWG  6.2974 5.7535
IC6201  VRD120Sch_LogicDeck2.DWG  6.2974 5.7535

IC6203  VRD100Sch_LogicDeck2.DWG  7.9343 -4.6440
IC6203  VRD120Sch_LogicDeck2.DWG  7.9343 -4.6440

IC6205  VRD120Sch_LogicDeck2.DWG  -5.0640 8.8360
IC6205  VRD100Sch_LogicDeck2.DWG  -5.0640 8.8360

IC6206  VRD120Sch_LogicDeck2.DWG  18.1591 1.3261
IC6206  VRD100Sch_LogicDeck2.DWG  18.1591 1.3261

IC6MC1  VRD100Sch_LogicDeck1.DWG  18.6560 2.9560
IC6MC1  VRD120Sch_LogicDeck1.DWG  18.6560 2.9560
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JK701  VRD120Sch_FunctionTimer.DWG  -40.9224 17.5825
JK701  VRD100Sch_FunctionTimer.DWG  -1.4824 16.8700

L6101  VRD120Sch_LogicDeck1.DWG  -5.5400 10.2760
L6101  VRD100Sch_LogicDeck1.DWG  -5.5400 10.2760

L6201  VRD120Sch_LogicDeck2.DWG  -7.2200 10.2760
L6201  VRD100Sch_LogicDeck2.DWG  -7.2200 10.2760

L6CM01  VRD100Sch_LogicDeck1.DWG  15.9257 -1.4152
L6CM01  VRD120Sch_LogicDeck1.DWG  15.9257 -1.4152

L6CM02  VRD100Sch_LogicDeck1.DWG  20.8220 1.0970
L6CM02  VRD120Sch_LogicDeck1.DWG  20.8220 1.0970

L701  VRD120Sch_LogicDeck1.DWG  -3.1360 -3.9640
L701  VRD100Sch_LogicDeck1.DWG  -3.1360 -3.9640

L702  VRD100Sch_FunctionTimer.DWG  37.7580 15.2530
L702  VRD120Sch_FunctionTimer.DWG  -16.1000 23.7211

LD6101  VRD120Sch_LogicDeck1.DWG  21.6840 7.7000
LD6101  VRD100Sch_LogicDeck1.DWG  21.6840 7.7000

LD6201  VRD120Sch_LogicDeck2.DWG  21.6840 7.7000
LD6201  VRD100Sch_LogicDeck2.DWG  21.6840 7.7000

PT6101  VRD120Sch_LogicDeck1.DWG  20.5681 12.8854
PT6101  VRD100Sch_LogicDeck1.DWG  20.5681 12.8854

PT6102  VRD100Sch_LogicDeck1.DWG  17.6264 12.8854
PT6102  VRD120Sch_LogicDeck1.DWG  17.6264 12.8854

PT6201  VRD120Sch_LogicDeck2.DWG  20.5681 12.8854
PT6201  VRD100Sch_LogicDeck2.DWG  20.5681 12.8854

PT6202  VRD100Sch_LogicDeck2.DWG  17.6264 12.8854
PT6202  VRD120Sch_LogicDeck2.DWG  17.6264 12.8854

Q6CM01  VRD100Sch_LogicDeck1.DWG  15.9250 3.9550
Q6CM01  VRD120Sch_LogicDeck1.DWG  15.9250 3.9550

Q701  VRD120Sch_FunctionTimer.DWG  -25.2779 34.6775
Q701  VRD100Sch_FunctionTimer.DWG  39.2021 33.2850

Q702  VRD120Sch_FunctionTimer.DWG  -24.2300 34.0375
Q702  VRD100Sch_FunctionTimer.DWG  40.2500 32.6450

Q703  VRD100Sch_FunctionTimer.DWG  41.3300 32.0050
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Q703  VRD120Sch_FunctionTimer.DWG  -23.1500 33.3975

Q704  VRD120Sch_FunctionTimer.DWG  -22.0754 32.7575
Q704  VRD100Sch_FunctionTimer.DWG  42.4046 31.3650

Q705  VRD120Sch_FunctionTimer.DWG  -20.9900 32.1175
Q705  VRD100Sch_FunctionTimer.DWG  43.4900 30.7250

Q706  VRD100Sch_FunctionTimer.DWG  38.0900 33.9250
Q706  VRD120Sch_FunctionTimer.DWG  -26.3900 35.3175

Q707  VRD100Sch_FunctionTimer.DWG  33.7700 36.4850
Q707  VRD120Sch_FunctionTimer.DWG  -30.7100 37.8775

Q708  VRD120Sch_FunctionTimer.DWG  -29.6300 37.2375
Q708  VRD100Sch_FunctionTimer.DWG  34.8500 35.8450

Q709  VRD120Sch_FunctionTimer.DWG  -28.5046 36.5975
Q709  VRD100Sch_FunctionTimer.DWG  35.9754 35.2050

Q710  VRD120Sch_FunctionTimer.DWG  -27.4700 35.9575
Q710  VRD100Sch_FunctionTimer.DWG  37.0100 34.5650

Q711  VRD100Sch_LogicDeck1.DWG  12.0493 -3.8850
Q711  VRD120Sch_LogicDeck1.DWG  12.0493 -3.8850

Q712  VRD120Sch_LogicDeck1.DWG  12.0300 -3.2450
Q712  VRD100Sch_LogicDeck1.DWG  12.0300 -3.2450

Q713  VRD120Sch_LogicDeck1.DWG  12.0300 -2.6450
Q713  VRD100Sch_LogicDeck1.DWG  12.0300 -2.6450

Q714  VRD100Sch_LogicDeck1.DWG  12.0268 -2.0050
Q714  VRD120Sch_LogicDeck1.DWG  12.0268 -2.0050

Q715  VRD100Sch_LogicDeck1.DWG  12.0300 -1.3650
Q715  VRD120Sch_LogicDeck1.DWG  12.0300 -1.3650

Q716  VRD120Sch_LogicDeck1.DWG  12.0300 -0.7650
Q716  VRD100Sch_LogicDeck1.DWG  12.0300 -0.7650

Q717  VRD120Sch_LogicDeck1.DWG  12.0300 -0.1650
Q717  VRD100Sch_LogicDeck1.DWG  12.0300 -0.1650

Q718  VRD100Sch_LogicDeck1.DWG  12.0300 0.5150
Q718  VRD120Sch_LogicDeck1.DWG  12.0300 0.5150

R1004  VRD100-120Sch_HiFiDeck1.DWG  20.2650 34.7730
R1004  VRD100-120Sch_HiFiDeck1.DWG  33.0250 39.0530
R1004  VRD100-120Sch_HiFiDeck1.DWG  22.1450 34.6530
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R1SD14  VRD100-120Sch_SMPS.DWG  15.8914 10.3099
R1SD14  VRD100-120Sch_SMPS.DWG  16.6543 23.6699

R2001  VRD100-120Sch_HiFiDeck1.DWG  17.5493 32.1330
R2001  VRD100-120Sch_HiFiDeck1.DWG  34.3433 49.2930

R3103  VRD100-120Sch_AVDeck1.DWG  32.5400 19.4386
R3103  VRD100-120Sch_AVDeck2.DWG  -187.6095 19.4386

R6101  VRD120Sch_LogicDeck1.DWG  23.1908 10.1800
R6101  VRD100Sch_LogicDeck1.DWG  23.1908 10.1800

R6102  VRD100Sch_LogicDeck1.DWG  7.5000 -0.7700
R6102  VRD120Sch_LogicDeck1.DWG  7.5000 -0.7700

R6103  VRD100Sch_LogicDeck1.DWG  7.2200 -0.7700
R6103  VRD120Sch_LogicDeck1.DWG  7.2200 -0.7700

R6104  VRD100Sch_LogicDeck1.DWG  6.9400 -0.7751
R6104  VRD120Sch_LogicDeck1.DWG  6.9400 -0.7751

R6106  VRD100Sch_LogicDeck1.DWG  5.2600 -0.7700
R6106  VRD120Sch_LogicDeck1.DWG  5.2600 -0.7700

R6107  VRD120Sch_LogicDeck1.DWG  4.9800 -0.7700
R6107  VRD100Sch_LogicDeck1.DWG  4.9800 -0.7700

R6108  VRD120Sch_LogicDeck1.DWG  3.8600 -0.7700
R6108  VRD100Sch_LogicDeck1.DWG  3.8600 -0.7700

R6109  VRD120Sch_LogicDeck1.DWG  4.1400 -0.7649
R6109  VRD100Sch_LogicDeck1.DWG  4.1400 -0.7649

R6110  VRD120Sch_LogicDeck1.DWG  4.4200 -0.7700
R6110  VRD100Sch_LogicDeck1.DWG  4.4200 -0.7700

R6111  VRD100Sch_LogicDeck1.DWG  4.7000 -0.7391
R6111  VRD120Sch_LogicDeck1.DWG  4.7000 -0.7391

R6112  VRD120Sch_LogicDeck1.DWG  19.5100 12.0179
R6112  VRD100Sch_LogicDeck1.DWG  19.5100 12.0179

R6113  VRD120Sch_LogicDeck1.DWG  19.3421 10.3500
R6113  VRD100Sch_LogicDeck1.DWG  19.3421 10.3500

R6114  VRD120Sch_LogicDeck1.DWG  0.7071 12.1358
R6114  VRD100Sch_LogicDeck1.DWG  0.7071 12.1358

R6115  VRD100Sch_LogicDeck1.DWG  0.3523 11.0958
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R6115  VRD120Sch_LogicDeck1.DWG  0.3523 11.0958

R6116  VRD100Sch_LogicDeck1.DWG  15.9152 12.7012
R6116  VRD120Sch_LogicDeck1.DWG  15.9152 12.7012

R6117  VRD100Sch_LogicDeck1.DWG  15.9152 11.4212
R6117  VRD120Sch_LogicDeck1.DWG  15.9152 11.4212

R6122  VRD100Sch_LogicDeck1.DWG  17.9952 6.1400
R6122  VRD120Sch_LogicDeck1.DWG  17.9952 6.1400

R6123  VRD120Sch_LogicDeck1.DWG  21.7523 8.6179
R6123  VRD100Sch_LogicDeck1.DWG  21.7523 8.6179

R6125  VRD100Sch_LogicDeck1.DWG  11.1900 7.4200
R6125  VRD120Sch_LogicDeck1.DWG  11.1900 7.4200

R6127  VRD120Sch_LogicDeck1.DWG  10.3500 7.1000
R6127  VRD100Sch_LogicDeck1.DWG  10.3500 7.1000

R6128  VRD120Sch_LogicDeck1.DWG  11.6621 6.2723
R6128  VRD100Sch_LogicDeck1.DWG  11.6621 6.2723

R6129  VRD120Sch_LogicDeck1.DWG  12.2221 6.2774
R6129  VRD100Sch_LogicDeck1.DWG  12.2221 6.2774

R6130  VRD100Sch_LogicDeck1.DWG  6.6621 11.9123
R6130  VRD120Sch_LogicDeck1.DWG  6.6621 11.9123

R6131  VRD100Sch_LogicDeck1.DWG  8.0600 10.8307
R6131  VRD120Sch_LogicDeck1.DWG  8.0600 10.8307

R6132  VRD120Sch_LogicDeck1.DWG  2.0352 0.7800
R6132  VRD100Sch_LogicDeck1.DWG  2.0352 0.7800

R6133  VRD120Sch_LogicDeck1.DWG  7.7800 11.5049
R6133  VRD100Sch_LogicDeck1.DWG  7.7800 11.5049

R6134  VRD100Sch_LogicDeck1.DWG  15.8671 9.3379
R6134  VRD120Sch_LogicDeck1.DWG  15.8671 9.3379

R6135  VRD100Sch_LogicDeck1.DWG  18.3122 9.3379
R6135  VRD120Sch_LogicDeck1.DWG  18.3122 9.3379

R6136  VRD100Sch_LogicDeck1.DWG  -7.6848 2.7400
R6136  VRD120Sch_LogicDeck1.DWG  -7.6848 2.7400

R6137  VRD100Sch_LogicDeck1.DWG  0.3952 10.2992
R6137  VRD120Sch_LogicDeck1.DWG  0.3952 10.2992
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R6138  VRD120Sch_LogicDeck1.DWG  0.8352 0.4600
R6138  VRD100Sch_LogicDeck1.DWG  0.8352 0.4600

R6139  VRD100Sch_LogicDeck1.DWG  7.1000 12.5974
R6139  VRD120Sch_LogicDeck1.DWG  7.1000 12.5974

R6140  VRD100Sch_LogicDeck1.DWG  4.7021 11.9991
R6140  VRD120Sch_LogicDeck1.DWG  4.7021 11.9991

R6141  VRD120Sch_LogicDeck1.DWG  4.4221 12.1100
R6141  VRD100Sch_LogicDeck1.DWG  4.4221 12.1100

R6142  VRD120Sch_LogicDeck1.DWG  3.8323 -1.9821
R6142  VRD100Sch_LogicDeck1.DWG  3.8323 -1.9821

R6143  VRD120Sch_LogicDeck1.DWG  3.8323 -4.2621
R6143  VRD100Sch_LogicDeck1.DWG  3.8323 -4.2621

R6144  VRD100Sch_LogicDeck1.DWG  4.7021 -2.2900
R6144  VRD120Sch_LogicDeck1.DWG  4.7021 -2.2900

R6145  VRD120Sch_LogicDeck1.DWG  4.7021 -4.2477
R6145  VRD100Sch_LogicDeck1.DWG  4.7021 -4.2477

R6146  VRD100Sch_LogicDeck1.DWG  5.3021 -2.2900
R6146  VRD120Sch_LogicDeck1.DWG  5.3021 -2.2900

R6147  VRD100Sch_LogicDeck1.DWG  5.7900 -4.2621
R6147  VRD120Sch_LogicDeck1.DWG  5.7900 -4.2621

R6148  VRD100Sch_LogicDeck1.DWG  -7.6100 -0.0200
R6148  VRD120Sch_LogicDeck1.DWG  -7.6100 -0.0200

R6149  VRD120Sch_LogicDeck1.DWG  -3.2422 2.4213
R6149  VRD100Sch_LogicDeck1.DWG  -3.2422 2.4213

R6150  VRD100Sch_LogicDeck1.DWG  -2.2878 0.4579
R6150  VRD120Sch_LogicDeck1.DWG  -2.2878 0.4579

R6151  VRD100Sch_LogicDeck1.DWG  11.3306 11.5813
R6151  VRD120Sch_LogicDeck1.DWG  11.3306 11.5813

R6153  VRD120Sch_LogicDeck1.DWG  -8.7448 0.2600
R6153  VRD100Sch_LogicDeck1.DWG  -8.7448 0.2600

R6164  VRD120Sch_LogicDeck1.DWG  10.6674 2.1400
R6164  VRD100Sch_LogicDeck1.DWG  10.6674 2.1400

R6168  VRD100Sch_LogicDeck1.DWG  11.3152 10.8612
R6168  VRD120Sch_LogicDeck1.DWG  11.3152 10.8612
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R6169  VRD120Sch_LogicDeck1.DWG  -2.4874 10.2200
R6169  VRD100Sch_LogicDeck1.DWG  -2.4874 10.2200

R6170  VRD100Sch_LogicDeck1.DWG  -2.4900 10.6200
R6170  VRD120Sch_LogicDeck1.DWG  -2.4900 10.6200

R6171  VRD100Sch_LogicDeck1.DWG  -8.8694 2.7400
R6171  VRD120Sch_LogicDeck1.DWG  -8.8694 2.7400

R6172  VRD100Sch_LogicDeck1.DWG  -4.6878 10.6200
R6172  VRD120Sch_LogicDeck1.DWG  -4.6878 10.6200

R6173  VRD120Sch_LogicDeck1.DWG  2.0352 2.1400
R6173  VRD100Sch_LogicDeck1.DWG  2.0352 2.1400

R6174  VRD120Sch_LogicDeck1.DWG  -5.5729 5.2579
R6174  VRD100Sch_LogicDeck1.DWG  -5.5729 5.2579

R6188  VRD120Sch_LogicDeck1.DWG  20.9500 9.6200
R6188  VRD100Sch_LogicDeck1.DWG  20.9500 9.6200

R6201  VRD100Sch_LogicDeck2.DWG  0.2080 2.1800
R6201  VRD120Sch_LogicDeck2.DWG  0.2080 2.4600

R6202  VRD100Sch_LogicDeck2.DWG  0.2300 8.7800
R6202  VRD120Sch_LogicDeck2.DWG  0.2300 8.7800

R6203  VRD120Sch_LogicDeck2.DWG  1.3100 8.4200
R6203  VRD100Sch_LogicDeck2.DWG  1.3100 8.4200

R6204  VRD120Sch_LogicDeck2.DWG  0.1900 8.1000
R6204  VRD100Sch_LogicDeck2.DWG  0.1900 8.1000

R6205  VRD100Sch_LogicDeck2.DWG  1.2726 7.7400
R6205  VRD120Sch_LogicDeck2.DWG  1.2726 7.7400

R6211  VRD120Sch_LogicDeck2.DWG  1.8080 5.1000
R6211  VRD100Sch_LogicDeck2.DWG  2.0776 4.8321

R6212  VRD100Sch_LogicDeck2.DWG  19.5100 12.0179
R6212  VRD120Sch_LogicDeck2.DWG  19.5100 12.0179

R6213  VRD120Sch_LogicDeck2.DWG  19.3421 10.8540
R6213  VRD100Sch_LogicDeck2.DWG  19.3421 10.8540

R6214  VRD120Sch_LogicDeck2.DWG  0.7071 12.1358
R6214  VRD100Sch_LogicDeck2.DWG  0.7071 12.1358

R6215  VRD100Sch_LogicDeck2.DWG  0.3523 11.0958
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R6215  VRD120Sch_LogicDeck2.DWG  0.3523 11.0958

R6216  VRD120Sch_LogicDeck2.DWG  15.9152 12.7012
R6216  VRD100Sch_LogicDeck2.DWG  15.9152 12.7012

R6217  VRD100Sch_LogicDeck2.DWG  15.9152 11.4212
R6217  VRD120Sch_LogicDeck2.DWG  15.9152 11.4212

R6218  VRD100Sch_LogicDeck2.DWG  1.2700 3.5000
R6218  VRD120Sch_LogicDeck2.DWG  1.2700 3.5000

R6219  VRD120Sch_LogicDeck2.DWG  -2.9226 6.5779
R6219  VRD100Sch_LogicDeck2.DWG  -2.9226 6.5779

R6221  VRD120Sch_LogicDeck2.DWG  -6.0169 5.3779
R6221  VRD100Sch_LogicDeck2.DWG  -6.0169 5.3779

R6222  VRD120Sch_LogicDeck2.DWG  17.9952 6.1400
R6222  VRD100Sch_LogicDeck2.DWG  17.9952 6.1400

R6223  VRD100Sch_LogicDeck2.DWG  21.7523 8.6179
R6223  VRD120Sch_LogicDeck2.DWG  21.7523 8.6179

R6224  VRD100Sch_LogicDeck2.DWG  11.1874 7.4200
R6224  VRD120Sch_LogicDeck2.DWG  11.1874 7.4200

R6225  VRD120Sch_LogicDeck2.DWG  10.3500 7.1000
R6225  VRD100Sch_LogicDeck2.DWG  10.3500 7.1000

R6226  VRD120Sch_LogicDeck2.DWG  10.3900 -1.9400
R6226  VRD100Sch_LogicDeck2.DWG  10.3900 -1.9400

R6227  VRD100Sch_LogicDeck2.DWG  9.0723 -2.0221
R6227  VRD120Sch_LogicDeck2.DWG  9.0723 -2.0221

R6228  VRD100Sch_LogicDeck2.DWG  10.4752 -3.3788
R6228  VRD120Sch_LogicDeck2.DWG  10.4752 -3.3788

R6229  VRD100Sch_LogicDeck2.DWG  10.4778 -3.8988
R6229  VRD120Sch_LogicDeck2.DWG  10.4778 -3.8988

R6230  VRD100Sch_LogicDeck2.DWG  6.6621 11.9123
R6230  VRD120Sch_LogicDeck2.DWG  6.6621 11.9123

R6231  VRD100Sch_LogicDeck2.DWG  8.0600 10.8307
R6231  VRD120Sch_LogicDeck2.DWG  8.0600 10.8307

R6232  VRD100Sch_LogicDeck2.DWG  2.0352 0.7800
R6232  VRD120Sch_LogicDeck2.DWG  2.0352 0.7800
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R6233  VRD120Sch_LogicDeck2.DWG  7.7800 11.5049
R6233  VRD100Sch_LogicDeck2.DWG  7.7800 11.5049

R6234  VRD100Sch_LogicDeck2.DWG  15.8671 9.3379
R6234  VRD120Sch_LogicDeck2.DWG  15.8671 9.3379

R6235  VRD100Sch_LogicDeck2.DWG  -9.5648 2.7400
R6235  VRD120Sch_LogicDeck2.DWG  18.3122 9.3379
R6235  VRD100Sch_LogicDeck2.DWG  18.3122 9.3379

R6237  VRD120Sch_LogicDeck2.DWG  0.1926 6.1000
R6237  VRD100Sch_LogicDeck2.DWG  0.1926 6.1000

R6238  VRD100Sch_LogicDeck2.DWG  0.8352 0.4600
R6238  VRD120Sch_LogicDeck2.DWG  0.8352 0.4600

R6239  VRD100Sch_LogicDeck2.DWG  7.1000 12.5974
R6239  VRD120Sch_LogicDeck2.DWG  7.1000 12.5974

R6240  VRD120Sch_LogicDeck2.DWG  17.2723 3.2979
R6240  VRD100Sch_LogicDeck2.DWG  17.2723 3.2979

R6241  VRD100Sch_LogicDeck2.DWG  4.4221 12.1100
R6241  VRD120Sch_LogicDeck2.DWG  4.4221 12.1100

R6242  VRD100Sch_LogicDeck2.DWG  3.2323 -1.9821
R6242  VRD120Sch_LogicDeck2.DWG  3.2323 -1.9821

R6243  VRD100Sch_LogicDeck2.DWG  3.2323 -4.2621
R6243  VRD120Sch_LogicDeck2.DWG  3.2323 -4.2621

R6244  VRD120Sch_LogicDeck2.DWG  4.1021 -2.2900
R6244  VRD100Sch_LogicDeck2.DWG  4.1021 -2.2900

R6245  VRD120Sch_LogicDeck2.DWG  4.1021 -4.2477
R6245  VRD100Sch_LogicDeck2.DWG  4.1021 -4.2477

R6246  VRD120Sch_LogicDeck2.DWG  4.7021 -2.2900
R6246  VRD100Sch_LogicDeck2.DWG  4.7021 -2.2900

R6247  VRD100Sch_LogicDeck2.DWG  5.1900 -4.2621
R6247  VRD120Sch_LogicDeck2.DWG  5.1900 -4.2621

R6248  VRD100Sch_LogicDeck2.DWG  -9.4900 0.3800
R6248  VRD120Sch_LogicDeck2.DWG  -9.4900 0.3800

R6249  VRD120Sch_LogicDeck2.DWG  4.7021 12.0043
R6249  VRD100Sch_LogicDeck2.DWG  4.7021 12.0043

R6250  VRD100Sch_LogicDeck2.DWG  -3.8902 0.4579
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R6250  VRD120Sch_LogicDeck2.DWG  -3.8902 0.4579

R6251  VRD100Sch_LogicDeck2.DWG  11.3306 11.5813
R6251  VRD120Sch_LogicDeck2.DWG  11.3306 11.5813

R6252  VRD120Sch_LogicDeck2.DWG  11.3152 9.7800
R6252  VRD100Sch_LogicDeck2.DWG  11.3152 9.7800

R6253  VRD120Sch_LogicDeck2.DWG  -10.2648 0.2600
R6253  VRD100Sch_LogicDeck2.DWG  -10.2648 0.2600

R6261  VRD100Sch_LogicDeck2.DWG  7.7800 -0.7391
R6261  VRD120Sch_LogicDeck2.DWG  7.7800 -0.7391

R6262  VRD100Sch_LogicDeck2.DWG  8.0600 -0.7700
R6262  VRD120Sch_LogicDeck2.DWG  8.0600 -0.7700

R6263  VRD120Sch_LogicDeck2.DWG  8.3400 -0.7700
R6263  VRD100Sch_LogicDeck2.DWG  8.3400 -0.7700

R6264  VRD100Sch_LogicDeck2.DWG  10.6674 2.7800
R6264  VRD120Sch_LogicDeck2.DWG  10.6674 2.7800

R6269  VRD100Sch_LogicDeck2.DWG  -4.1674 10.2200
R6269  VRD120Sch_LogicDeck2.DWG  -4.1674 10.2200

R6270  VRD120Sch_LogicDeck2.DWG  -4.1700 10.6200
R6270  VRD100Sch_LogicDeck2.DWG  -4.1700 10.6200

R6271  VRD120Sch_LogicDeck2.DWG  -10.7494 2.7400
R6271  VRD100Sch_LogicDeck2.DWG  -10.7494 2.7400

R6272  VRD120Sch_LogicDeck2.DWG  -6.3678 10.6200
R6272  VRD100Sch_LogicDeck2.DWG  -6.3678 10.6200

R6273  VRD120Sch_LogicDeck2.DWG  2.0352 2.1400
R6273  VRD100Sch_LogicDeck2.DWG  2.0352 2.1400

R6274  VRD100Sch_LogicDeck2.DWG  -7.2929 5.2579
R6274  VRD120Sch_LogicDeck2.DWG  -7.2929 5.2579

R6276  VRD100Sch_LogicDeck2.DWG  1.2726 6.4600
R6276  VRD120Sch_LogicDeck2.DWG  1.2726 6.4600

R6277  VRD120Sch_LogicDeck2.DWG  5.5421 11.3191
R6277  VRD100Sch_LogicDeck2.DWG  5.5421 11.3191

R6288  VRD100Sch_LogicDeck2.DWG  20.9500 9.6200
R6288  VRD120Sch_LogicDeck2.DWG  20.9500 9.6200
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R6CM02  VRD120Sch_LogicDeck1.DWG  16.0840 1.3780
R6CM02  VRD100Sch_LogicDeck1.DWG  16.0840 1.3780

R6CM03  VRD100Sch_LogicDeck1.DWG  19.8825 3.4985
R6CM03  VRD120Sch_LogicDeck1.DWG  19.8825 3.4985

R6CM04  VRD100Sch_LogicDeck1.DWG  16.2840 0.3000
R6CM04  VRD120Sch_LogicDeck1.DWG  16.2840 0.3000

R6CM05  VRD100Sch_LogicDeck1.DWG  16.1266 -0.0200
R6CM05  VRD120Sch_LogicDeck1.DWG  16.1266 -0.0200

R6CM06  VRD120Sch_LogicDeck1.DWG  16.1977 -0.4221
R6CM06  VRD100Sch_LogicDeck1.DWG  16.1977 -0.4221

R6CM07  VRD100Sch_LogicDeck1.DWG  17.4267 -2.3170
R6CM07  VRD120Sch_LogicDeck1.DWG  17.4267 -2.3170

R6CM08  VRD120Sch_LogicDeck1.DWG  21.5240 0.1000
R6CM08  VRD100Sch_LogicDeck1.DWG  21.5240 0.1000

R6CM09  VRD120Sch_LogicDeck1.DWG  21.0258 -0.7346
R6CM09  VRD100Sch_LogicDeck1.DWG  21.0258 -0.8200

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R6CM11  VRD100Sch_LogicDeck1.DWG  21.1351 -0.5879

R6CM14  VRD100Sch_LogicDeck1.DWG  16.0836 1.0175
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R6CM15  VRD120Sch_LogicDeck1.DWG  16.9240 4.0600
R6CM15  VRD100Sch_LogicDeck1.DWG  16.9240 4.0600

R6CM16  VRD120Sch_LogicDeck1.DWG  17.1027 3.2179
R6CM16  VRD100Sch_LogicDeck1.DWG  17.1027 3.2179

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R6CM17  VRD120Sch_LogicDeck1.DWG  16.0627 3.2179

R711  VRD120Sch_LogicDeck1.DWG  10.9281 -3.8988
R711  VRD100Sch_LogicDeck1.DWG  10.9281 -3.8988

R712  VRD100Sch_LogicDeck1.DWG  10.9126 -3.2187
R712  VRD120Sch_LogicDeck1.DWG  10.9126 -3.2187

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R713  VRD100Sch_LogicDeck1.DWG  10.9126 -2.6187
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R716  VRD120Sch_LogicDeck1.DWG  10.9126 -0.7388

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R717  VRD120Sch_LogicDeck1.DWG  10.9126 -0.1387

R718  VRD100Sch_LogicDeck1.DWG  10.9126 0.5012
R718  VRD120Sch_LogicDeck1.DWG  10.9126 0.5012

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R719  VRD120Sch_FunctionTimer.DWG  -28.0314 24.9824

R720  VRD120Sch_FunctionTimer.DWG  -29.8800 24.9824
R720  VRD100Sch_FunctionTimer.DWG  49.4952 22.5099

R721  VRD120Sch_FunctionTimer.DWG  -54.6393 34.7400
R721  VRD100Sch_FunctionTimer.DWG  9.8407 33.3475

R722  VRD100Sch_FunctionTimer.DWG  9.8150 32.7075
R722  VRD120Sch_FunctionTimer.DWG  -54.6650 34.1000

R723  VRD120Sch_FunctionTimer.DWG  -54.6650 33.4600
R723  VRD100Sch_FunctionTimer.DWG  9.8150 32.0675

R724  VRD100Sch_FunctionTimer.DWG  9.8107 31.4275
R724  VRD120Sch_FunctionTimer.DWG  -54.6693 32.8200

R725  VRD100Sch_FunctionTimer.DWG  9.8150 30.7875
R725  VRD120Sch_FunctionTimer.DWG  -54.6650 32.1800

R726  VRD100Sch_FunctionTimer.DWG  9.8150 33.9875
R726  VRD120Sch_FunctionTimer.DWG  -54.6650 35.3800

R727  VRD120Sch_FunctionTimer.DWG  -54.6650 37.9400
R727  VRD100Sch_FunctionTimer.DWG  9.8150 36.5475

R728  VRD100Sch_FunctionTimer.DWG  9.8150 35.9075
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R729  VRD120Sch_FunctionTimer.DWG  -54.6607 36.6600
R729  VRD100Sch_FunctionTimer.DWG  9.8193 35.2675

R730  VRD120Sch_FunctionTimer.DWG  -54.6650 36.0200
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R731  VRD120Sch_FunctionTimer.DWG  -54.6393 31.2125
R731  VRD100Sch_FunctionTimer.DWG  9.8407 29.8200

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R732  VRD100Sch_FunctionTimer.DWG  9.8150 29.1800

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R733  VRD120Sch_FunctionTimer.DWG  -54.6650 29.9325

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R738  VRD100Sch_FunctionTimer.DWG  9.8150 25.3400

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R740  VRD120Sch_FunctionTimer.DWG  -22.8346 44.3612

R741  VRD100Sch_LogicDeck2.DWG  12.2221 6.3520
R741  VRD120Sch_LogicDeck2.DWG  12.2221 6.3520

R742  VRD100Sch_LogicDeck2.DWG  11.6621 6.3211
R742  VRD120Sch_LogicDeck2.DWG  11.5721 6.3211

R743  VRD100Sch_FunctionTimer.DWG  31.4938 14.0977
R743  VRD120Sch_FunctionTimer.DWG  -21.9080 22.2751

R744  VRD100Sch_FunctionTimer.DWG  7.4066 14.3099
R744  VRD120Sch_FunctionTimer.DWG  -35.3134 14.9099

RM701  VRD100Sch_FunctionTimer.DWG  29.2780 17.2130
RM701  VRD120Sch_FunctionTimer.DWG  -27.2884 22.8380

S6101  VRD120Sch_LogicDeck1.DWG  17.6732 7.2861
S6101  VRD100Sch_LogicDeck1.DWG  17.6732 7.2861

S6102  VRD100Sch_LogicDeck1.DWG  20.1349 7.8728
S6102  VRD120Sch_LogicDeck1.DWG  20.1349 7.8728
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S6201  VRD120Sch_LogicDeck2.DWG  17.6732 7.2861
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S6202  VRD100Sch_LogicDeck2.DWG  20.1349 7.8728
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SW6102  VRD120Sch_LogicDeck1.DWG  19.6359 5.7635
SW6102  VRD100Sch_LogicDeck1.DWG  19.6359 5.7635

SW6103  VRD100Sch_LogicDeck1.DWG  6.0160 -2.2040
SW6103  VRD120Sch_LogicDeck1.DWG  6.0160 -2.2040

SW6202  VRD120Sch_LogicDeck2.DWG  19.6359 5.7635
SW6202  VRD100Sch_LogicDeck2.DWG  19.6359 5.7635

SW6203  VRD120Sch_LogicDeck2.DWG  5.9960 -3.2160
SW6203  VRD100Sch_LogicDeck2.DWG  5.9960 -3.2160

SW701  VRD120Sch_FunctionTimer.DWG  -64.6083 32.1255
SW701  VRD100Sch_FunctionTimer.DWG  6.0317 31.6530

SW702  VRD100Sch_FunctionTimer.DWG  5.9937 33.9330
SW702  VRD120Sch_FunctionTimer.DWG  -64.6463 36.3655

SW703  VRD100Sch_FunctionTimer.DWG  2.1937 29.9730
SW703  VRD120Sch_FunctionTimer.DWG  -67.4863 32.1255

SW704  VRD100Sch_FunctionTimer.DWG  5.9873 28.0930
SW704  VRD120Sch_FunctionTimer.DWG  -56.1727 22.3730

SW706  VRD100Sch_FunctionTimer.DWG  2.1937 28.0930
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SW707  VRD100Sch_FunctionTimer.DWG  2.1937 25.1330
SW707  VRD120Sch_FunctionTimer.DWG  -14.2063 19.4455

SW708  VRD120Sch_FunctionTimer.DWG  -51.8063 22.3730
SW708  VRD100Sch_FunctionTimer.DWG  2.1937 22.7330

SW709  VRD120Sch_FunctionTimer.DWG  -14.2000 17.1255
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SW710  VRD100Sch_FunctionTimer.DWG  43.0337 18.7330
SW710  VRD120Sch_FunctionTimer.DWG  -17.5663 17.1255

SW711  VRD100Sch_FunctionTimer.DWG  46.9517 16.4130
SW711  VRD120Sch_FunctionTimer.DWG  -21.4083 19.4455

SW712  VRD100Sch_FunctionTimer.DWG  46.9137 18.7330
SW712  VRD120Sch_FunctionTimer.DWG  -17.5663 19.4455
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SW713 VRD120Sch_FunctionTimer.DWG -21.4463 17.1255
SW713 VRD100Sch_FunctionTimer.DWG 50.2737 18.7330

TP6103 VRD120Sch_LogicDeck1.DWG 6.8160 14.1560
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TP6201 VRD100Sch_LogicDeck2.DWG 6.8377 14.1560
TP6201 VRD100Sch_LogicDeck2.DWG 0.8560 2.9960
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XT6101 VRD100Sch_LogicDeck1.DWG 1.8840 4.1400
XT6101 VRD120Sch_LogicDeck1.DWG 1.8840 4.1400

XT6201 VRD100Sch_LogicDeck2.DWG 1.0440 4.4200
XT6201 VRD120Sch_LogicDeck2.DWG 1.0440 4.4200

XT6202 VRD120Sch_LogicDeck2.DWG 1.0440 5.4200
XT6202 VRD100Sch_LogicDeck2.DWG 1.8441 5.4665

ZD701 VRD100Sch_FunctionTimer.DWG 10.1300 39.9838

--- Symbols found in schematics and not in parts list ---

C5219, VRD100-120Sch_HiFiDeck2.DWG, 52.6100 52.7400
CN6125, VRD100Sch_LogicDeck1.DWG, -6.0360 13.1800
CN6125, VRD120Sch_LogicDeck1.DWG, -6.0558 13.1800
IC601, VRD100Sch_LogicDeck1.DWG, 6.2929 5.7535
IC6MC1, VRD100Sch_LogicDeck1.DWG, 18.6560 2.9560
IC6MC1, VRD120Sch_LogicDeck1.DWG, 18.6560 2.9560
J8MI, VRD100-120Sch_InputOutput.DWG, 115.1470 42.5750
R1004, VRD100-120Sch_HiFiDeck1.DWG, 20.2650 34.7730
R1004, VRD100-120Sch_HiFiDeck1.DWG, 33.0250 39.0530
R1004, VRD100-120Sch_HiFiDeck1.DWG, 22.1450 34.6530
R2001, VRD100-120Sch_HiFiDeck1.DWG, 17.5493 32.1330
R2001, VRD100-120Sch_HiFiDeck1.DWG, 34.3433 49.2930
R4M134, VRD100-120Sch_HiFiDeck2.DWG, 51.1400 58.0600
R6015, VRD100Sch_LogicDeck1.DWG, 6.6600 -0.7700
R6287, VRD100Sch_LogicDeck2.DWG, 4.7000 -0.7700

--- Symbols found in parts list and not in schematics ---

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S6202A, ELECTRICAL COMPONENTS (B)
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SCQ11A, ELECTRICAL COMPONENTS (A)
TM401B, ELECTRICAL COMPONENTS (B)
TM401B, ELECTRICAL COMPONENTS (B)
TP01, ELECTRICAL COMPONENTS (A)
TP3101, ELECTRICAL COMPONENTS (A)
TP3101, ELECTRICAL COMPONENTS (A)
TP6101, ELECTRICAL COMPONENTS (A)
TP6101, ELECTRICAL COMPONENTS (A)

TP803, ELECTRICAL COMPONENTS (B)
TP803, ELECTRICAL COMPONENTS (B)
TP804, ELECTRICAL COMPONENTS (B)
TP804, ELECTRICAL COMPONENTS (B)
TP805, ELECTRICAL COMPONENTS (B)
W2215, ELECTRICAL COMPONENTS (B)
W2215, ELECTRICAL COMPONENTS (B)
W706, FUNCTION/TIMER